

Figure 1 - FPGA blocks for User-specific functions and I/O interfaces

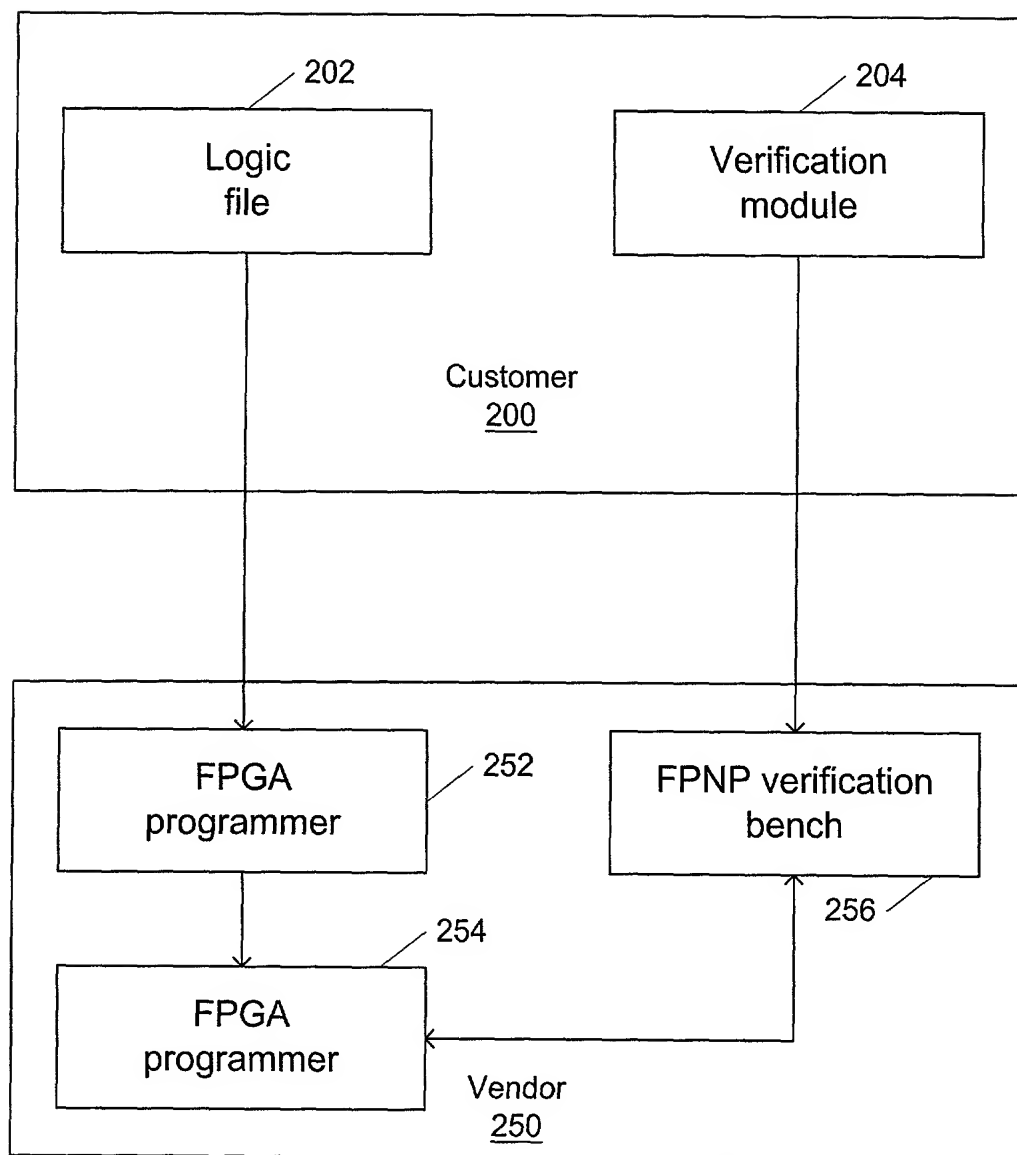


Fig. 2

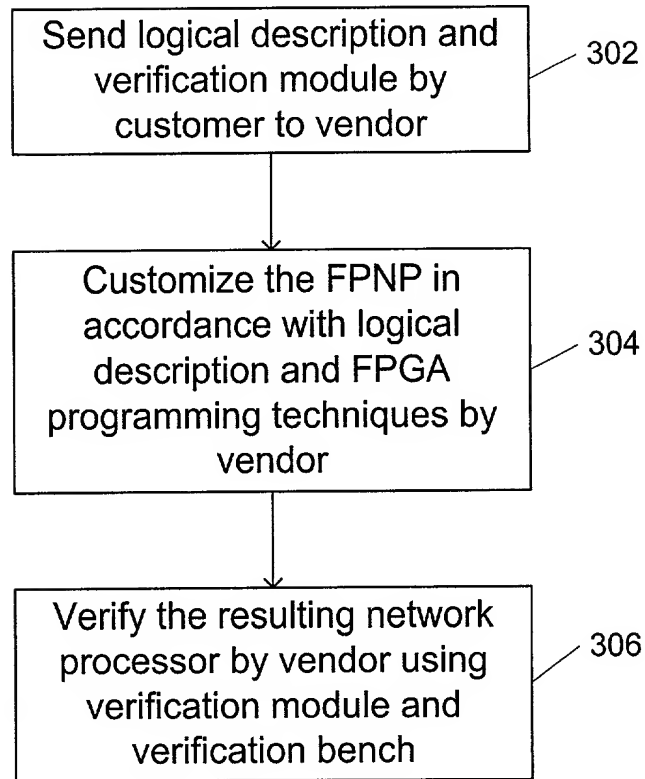


Fig. 3